

Sardar Vallabhbhai National Institute of Technology, Surat, India
Department of Electrical Engineering
B. TECH ODD SEMESTER MAKE-UP EXAMINATION (NOVEMBER - 2024)
Venue: New Seminar Hall (Department of Electrical Engineering)

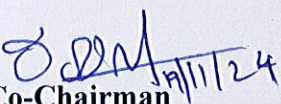
इलेक्ट्रिकल इंजीनियरिंग विभाग
Department of Electrical Engineering
स.व.स.पौ.सं., सुरत-७/S.V.N.I.T., Surat-7
आवक नं./INWARD No.:
जावक नं./OUTWARD No.: 1526
दिनांक/Date: 19/11/24


Date: 19/11/2024

Section Time	B. Tech I (I Sem.) 09:00 AM to 10:30 AM	B. Tech II (III Sem.) 11:00 AM to 12:30 PM	B. Tech. III (V Sem.) 02:00 PM to 03:30 PM	B. Tech. IV (VII Sem.) 04:00 PM to 05:30 PM
25/11/2024 (Monday)	PH109 - Fundamentals of Physics	EE 201 - Electrical Machines I	EE 307 - Electrical & electronic measurements	ES - VI : EE 451 - Switched Mode Power Supply ES - VI : EE 453 - Computer Method Power System
26/11/2024 (Tuesday)	EE101 - Basic Electrical Engineering	EE 203 - Signals & Systems	EE 303 - Control Systems	EE 403- Electrical Machine Design
27/11/2024 (Wednesday)	ME110 - Engineering Drawing*	EC209 - Digital Circuits	HU 301 - Professional Ethics, Economics & Management	ES - IV : EE 425 - Advanced Power Electronics ES - IV : EE 433 - Power System Operation & Control
28/11/2024 (Thursday)	MA113 – Mathematics - I	EE 251 - Renewable Energy Sources (Elective) EE 253 - Object-oriented Programming & Data Structure (Elective)	EE 301 - Power System Analysis	EE 401 - Microcontroller and Embedded 'C' Programming
29/11/2024 (Friday)	HS110 – English and Professional Communication*	EE 231 - Electromagnetic Theory	EIS I : EE 363 - Optimization Methods	EE 405 - Switch Gear and Protection
30/11/2024 (Saturday)	---	---	EE 305 - Power Electronic Converters	ES - V : EE 443 - Electric Vehicles ES - V : EE 445 - Digital Signal Processing

* Venue will be as per concerned Department sitting arrangement.

Note: The examination will be conducted as per the CIRCULAR No. Acad/ Make Up Test/1190/2024-25 Date: 18/11/2024, All the students have to compulsorily carry their Hall tickets, Id card during the examination. All the students are required to take their seats 15 minutes before the scheduled time.


Chairman/ Co-Chairman
(Examination committee)


HEAD, DoEE

Copy to: 1. All Heads & Faculty members (DoEE) through email, 2. Dean (Academic), 3. Director - For kind information, 4. Deputy Registrar (Academic), 5. Prof I/C Central Computer Centre (For display on institute website), 6. Chief Coordinator (Examination), 7. Student Notice Board.


ઇલેક્ટ્રિકલ ઇન્જિનિયરિંગ વિભાગ
Department of Electrical Engineering
સ.વ.રા.પો.સં., સુરત-૭/S.V.N.I.T., Surat-7
આવક નં./INWARD No: _____
જાવક નં./OUTWARD No: 1527
દિનાંક/Date: 19/11/24


Sardar Vallabhbhai National Institute of Technology, Surat, India
Department of Electrical Engineering
M.TECH 1st YEAR ODD SEMESTER MAKE-UP EXAMINATION (NOVEMBER - 2024)
Venue: New Seminar Hall (Department of Electrical Engineering)

Date: 19/11/2024

Section	M. Tech I (PEED)	M. Tech I (PS)	M. Tech I (CA)
Time	04:00 PM to 05:30 PM		
25/11/2024 (Monday)	EEPE105 - Solar and Wind Energy Conversion and Control	ELPS103 - Power System Protection	ELCA101 - Linear System Theory
26/11/2024 (Tuesday)	EEPE103 - Modelling of Electrical Machines and DC Drives	ELPS101 - Computer-Aided Power System Analysis	ELCA129 - Process dynamics & Control (Elective - 2)
27/11/2024 (Wednesday)	EEPE101 - Power Electronics - 1	ELPS131- Power System Transients (Elective - II)	ELCA103 – Robust & Optimal Control
28/11/2024 (Thursday)	EEPE141 - Adaptive Control and Soft Computing (Elective - II)	ELPS105 - Restructuring in Power System	ELCA105- Industrial Automation
29/11/2024 (Friday)	EEPE115 - Digital Signal Processing (Elective - I)	ELPS111 - Digital Signal Processing (Elective - I)	ELCA111- Digital Signal Processing (Elective - I)

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